AMENDMENTS TO THE CLAIMS

Claim 1.   (currently amended) A digital signal processing apparatus, comprising:

a plurality of digital signal processing blocks including at least a signal processing block for decoding data of streams, each of said plurality of digital signal processing blocks having a signal processor;

and a host arithmetic-operation processing block as functions necessary for processing a digital signalfor controlling said digital processing apparatus by outputting a command of a high layer and not on a real time basis; and

a bus for connecting said host arithmetic-operation processing block and said plurality of digital signal processing blocks for transferring said command and for transferring said data of streams,

wherein each of said signal processors interprets and executes said command.

wherein commands for controlling the operations of each of the blocks and data of streams are transferred through said bus.

Claim 2.   (original) The digital signal processing apparatus as set forth in claim 1, wherein said plurality of digital signal processing blocks include at least a front end block for processing a received signal of a digital broadcast.

Claims 3-5.   (canceled)

Claim 6.   (original) The digital signal processing apparatus as set forth in claim 1, wherein the command is described and embedded in a script of hypertext,

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wherein the hypertext is interpreted by a browser and a picture for operating the

extension function is displayed, and

wherein a command corresponding to the function is embedded and displayed in the

picture for operating the extension function.

Claim 7.   (original) The digital signal processing apparatus as set forth in claim 1,

wherein the data of streams contains video data and / or audio data.

Claim 8.   (original) The digital signal processing apparatus as set forth in claim 7,

wherein the video data and / or the audio data has been compressed.

Claim 9.   (original) The digital signal processing apparatus as set forth in claim 1,

wherein said bus is a general-purpose bus, and

wherein each block connected to said bus can be added or substituted.

Claim 10.   (original) The digital signal processing apparatus as set forth in claim 9,

wherein when each block connected to said bus is added or substituted, software for

operating the added or substituted block is automatically installed.

Claim 11.   (original) The digital signal processing apparatus as set forth in claim 9,

wherein software for operating the added or substituted block is stored in a memory

thereof, and

wherein when the block is added or substituted, the software stored in the memory is installed.

Claim 12.   (original) The digital signal processing apparatus as set forth in claim 9, wherein when each block connected to said bus is added or substituted, a service center is accessed through a telephone line, software for operating the added or substituted block is downloaded from the service center through the telephone line, and the downloaded software is installed.

Claim 13.   (currently amended) A digital signal processing method, comprising the steps of:

structuring functions necessary for processing a digital signal as a plurality of digital signal processing blocks including at least a signal processing block for decoding data of streams, each of said plurality of digital signal processing blocks having a signal processor and a host ~~arithmetic operation~~ processing block; ~~and~~

connecting the host ~~arithmetic operation~~ processing block and the plurality of digital signal processing blocks through a bus~~,~~; and

outputting and transferring a command through the bus for controlling said digital signal processing apparatus, said command being of a high layer and not on a real time basis;

wherein each of said signal processors interprets and executes said command and outputs said data of streams through the bus.

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~~wherein commands for controlling the operations of each of the blocks and data of streams are transferred through the bus.~~

Claim 14.   (original) The digital signal processing method as set forth in claim 13, wherein the plurality of digital signal processing blocks include at least a front end block for processing a received signal of a digital broadcast.

Claims 15-17. (canceled)

Claim 18.   (original) The digital signal processing method as set forth in claim 13, wherein the command is described and embedded in a script of hypertext.

Claim 19.   (original) The digital signal processing method as set forth in claim 13, wherein the data of streams contains video data and / or audio data.

Claim 20.   (original) The digital signal processing method as set forth in claim 19, wherein the video data and / or the audio data has been compressed.

Claim 21.   (original) The digital signal processing method as set forth in claim 13, wherein the bus is a general-purpose bus, and

wherein each block connected to the bus can be added or substituted.

Claim 22.   (original) The digital signal processing method as set forth in claim 21,

wherein when each block connected to the bus is added or substituted, software for

operating the added or substituted block is automatically installed.

Claim 23.   (original) The digital signal processing method as set forth in claim 21,

wherein software for operating the added or substituted block is stored in a memory

thereof, and

wherein when the block is added or substituted, the software stored in the memory is

installed.

Claim 24.   (original) The digital signal processing method as set forth in claim 21,

wherein when each block connected to the bus is added or substituted, a service center is

accessed through a telephone line, software for operating the added or substituted block is

downloaded from the service center through the telephone line, and the downloaded software is

installed.

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